

(SYLLABUS)

1.

(Course Title)	EDA Full-custom	(Instructor)			
(Year)	2026	(Semester)	2	(Course No.)	2150085301
(Class)	01	(Open to)	3, 4	(Course Classification)	-
/	3.0 / 03 / 3		100	가	가
(Office)	402	(Telephone)	02-828-7145	(e-mail)	joonchai@ssu.ac.kr
	(PBL)				
	(*) (ABEEK Classification)		(*) (ABEEK Requirement)		
(Course Description)	Full-custom Layout Simulation Layout				

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	30	30
	30	30
	30	30

(Required Texts)		
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2.

(Week)	(Keyword)	(Description)		(Texts)
01	Full-custom	○ ○ Full-custom		
02	Full-custom	○ Full-custom ○	' ' '	
03	NMOS/PMOS	○ NMOS ○ PMOS	' ' '	
04	Inverter/NAND	○ Inverter ○ NAND	' ' '	
05	NOR/MUX	○ NOR ○ MUX	' ' '	
06	Amplifier	○ Amplifier 1	' ' '	
07	Amplifier	○ Amplifier 2	' ' '	
08		○ ○ 1~7	' ' '	
09	Layout , Physical Verification(DRC, LVS), Design Rule, Post-layout Simulation	○ Layout , Design Rule ○ Physical Verification(DRC, LVS) ○ Post-layout Simulation		
10	Layout , Inverter Layout Design	○ Layout ○ Inverter Layout Design	' ' '	
11	NOR Layout Design, MUX Layout Design	○ NAND Layout Design ○ NOR Layout Design ○ MUX Layout Design	' ' '	
12	Amplifier Layout Design	○ Amplifier Layout Design ○ Physical Verification	' ' '	
13	RC Extraction, Post-Simulation	○ Inverter/NAND/NOR RC Extraction(LPE) ○ Inverter/NAND/NOR Post Simulation	' ' '	
14	RC Extraction, Post-Simulation	○ MUX/Amplifier RC Extractin(LPE) ○ MUX/Amplifier Post Simulation	' ' '	
15		○ ○ 9~14	' ' '	

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3. ()

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	Open-ended problem		
	Teamwork		
	Communication skills		